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VLSI VIVA Questions and Answers : 11) Mention what are three regions of operation of MOSFET and how are they used? 12) Explain what is the depletion region? 13) Explain why is the number of gate inputs to CMOS gates usually limited to four?

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VLSI LAB VIVA Questions. MOSFET has three regions of operations. 1.Cut-off region. 2.Triode region. 3.Saturation region. The triode and cut-off region are used to function as a switch, while, saturation region is used to operate as an amplifier.

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VLSI Lab viva question with answers. Note: First of all, we are

very thankful to the Only-Vlsi (http://only-vlsi.blogspot.in) for these question answers. 1. How do you convert a XOR gate into a buffer and a inverter (Use only one XOR gate for each)? Answer. 2. Implement an 2-input AND gate using a 2x1 mux. Answer. 3. What is a multiplexer?

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VLSI Lab viva question with answers0100, 0010, and 0001. If the value is 0100, then it means second state is the current state.One-Cold encoding: Same as one-hot encoding except that 0 is the valid value. If there are fourstates then it requires four bits (four flip-flops) to represent the current state.

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Vlsi lab Viva questions 1) Tell something about why we do gate level simulations? 2) Say if I perform Formal Verification say Logical Equivalence across Gatelevel netlists(Synthesis and post routed netlist). Do you still see a reason behind GLS.? 3)An AND gate and OR gate are given inputs X & 1 , what is expected output?

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1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the true state is denoted by the number one, referred as logic one or logic high. While, the false state is represented by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

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Electrical Circuits Lab Viva Questions and Answers Electrical Lab Edit Question No. 01: What id difference between power and energy? Answer: Energy is defined as the capacity of a physical system to perform work. In the context of electric circuits, energy (w) is related to power by the following relationship $p = vi = dw/dt$

Electrical Circuits Lab Viva Questions and Answers ...

VLSI Design Tutorial ; Question 11. Which Is The Default Delay In Vhdl? Answer : delta delay. VLSI Interview Questions ; Question 12. What Is Propagation Delay? Answer : Transport delay models the behavior of a wire, in which all pulses are propagatedirrespective there width. Perl Scripting Interview Questions; Question 13. What Is Inertial ...

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VLSI Lab Viva questions and answers 1. What is Intrinsic and Extrinsic Semiconductor? The pure Silicon is known as Intrinsic Semiconductor. When impurity is added with pure Silicon, its electrical properties are varied. This is known as Extrinsic Semiconductor. 2. What is CMOS Technology?

Vlsi Lab Viva Questions And Answers 1 [z06w8vvg8xqx]

A DB query is a code written in order to get the information back from the database. Query can be designed in such a way that it matched with our expectation of the result set. Simply, a question to the Database. 20. What is subquery? A subquery is a query within another query. The outer query is called as main query, and inner query is called ...

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Very-large-scale integration (VLSI) is the process of creating an integrated circuit (IC) by combining thousands of transistors into a single chip. VLSI began in the 1970s when complex semiconductor and communication technologies were being developed. The microprocessor is a VLSI device.. Before the introduction of VLSI technology, most ICs had a limited set of functions they could perform.

VLSI Design - Digital System - Tutorialspoint

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What are super, primary, candidate and foreign keys? Ans: A super key is a set of attributes of a relation schema upon which all attributes of the schema are functionally dependent. No two rows can have the same value of super key attributes. A Candidate key is minimal superkey, i.e., no proper subset of

Candidate key attributes can be a superkey. A Primary Key is one of the candidate keys.