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About the Author: KESHAB K. PARHI is Edgar F. Johnson Professor of Electrical and Computer Engineering at the University of Minne-sota in Minneapolis. Dr. Parhi is widely recognized for his work in the area of VLSI digital signal and image processing.

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Y. Lao and K.K. Parhi, "Obfuscating DSP Circuits via High-Level Transformations," IEEE Transactions on VLSI Systems, 23(5), pp. 819-830, May 2015 Z. Zhang and K.K. Parhi, "Low-Complexity Seizure Prediction From IEEG/sEEG using Spectral Power and Ratios of Spectral Power," IEEE Transactions on Biomedical Circuits and Systems, 10(3), pp. 693-706, June 2016

### Parhi, Keshab | Electrical and Computer Engineering

Brief Biography. Keshab K. Parhi received the B.Tech. (Honors) degree from the Indian Institute of Technology, Kharagpur (India) in 1982 in Electrical Engineering, the M.S. degree from the University of Pennsylvania in 1984 in Electrical Engineering, and the Ph.D. degree from the University of California, Berkeley in 1988 in Electrical Engineering and Computer Sciences.

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